Industry Testimonials

"I am thrilled to share that we have chosen RMK Group of Institutions as our Knowledge Partner for VLSI Design. This decision was made after a meticulous analysis of various peer institutions, and RMK stood out for several reasons. Their unwavering commitment to academic excellence, coupled with their innovative research endeavors, truly impressed us. RMK's state-of-the-art facilities and their special focus on nurturing top-tier talent align perfectly with our organization's vision of driving technological advancements and producing industry-ready graduates. By infusing industry expertise with academic excellence, RMK's dedication is truly commendable. We are confident that this collaboration will yield groundbreaking achievements in the semiconductor sector in the upcoming years."

"RMK Group of institutions left a lasting impression on us, and we are confident that our collaboration will be very much beneficial to the students. From the initial coordination of the visit to the personalized guidance provided throughout all the events, every staff member we met have exhibited professionalism and a genuine commitment to ensuring our visit was both productive and enjoyable. Their knowledge about the institution and willingness to address our queries reflected their dedication to fostering meaningful partnerships with us. We look forward to exploring potential avenues for collaboration and furthering our relationship with RMK Group of institutions."



GPSC-Talent Acquisition, Tech Mahindra, Bangalore

Mr. Prashanth Nagarajan, Director, Epical Layouts private



Alumni Testimonials



Mr. P.Senthil 2001-05 Batch Service: I.R.S(2010) ncome Tax Bengaluru

R.M.K Engineering College changed my values, nurtured my character, gave me enlightenment about the vision of life and enhanced my extracurricular activities. As a civil servant, I still admire the professionalism practiced in my college in managing the day-to-day affairs, dedication of my professors, well maintained infrastructures (including sports facilities, transport and mess) and the encouragement given for student's innovation. My College provided the platform to enter into Indian civil service and this divine opportunity to serve our Nation. It also taught me a unique mantra, "If you cannot enjoy the journey (learning), you can never enjoy the destination (life)!"



Batch: 2014-18 AWS Solution Architect, ech Mahindra, France

Planning is an important concept in Management. Planning in short is where we are at a point of time and where we want to be in the future. Both Planning and Ms. Keerthana Muralidharan Management not only applies for profession and career growth, it also applies for our life. Yes, you just need to decide joining the RMK group and the sculptors there plan your future as you want to be. For sure, you cannot have an escape because these sculptors are so determined to take you to the best on the earth and it is what I am today. The credit of my success is their dedication. I wanted to simply fly on an aircraft, but my teachers have taught me to design the wings of an aircraft. Pranams to the sculptors of RMK Group.

Alumni Meet at USA & UK









San Francisco (23.04.2019), Los Angeles (26.04.2019), Atlanta (29.04.2019), Washington DC (07.05.2019), New York (10.05.2019)









UK Alumni Meet 2023 (19.05.2023) London

R.M.K. GROUP OF INSTITUTIONS

ACCREDITED BY NAAC I ISO 21001:2018 CERTIFIED INSTITUTION

ALL THE ELIGIBLE UG PROGRAMS ARE ACCREDITED BY NBA, NEW DELHI.

For Further Details Visit: www.rmkec.ac.in

To know more about B.E.EE (VLSI)

RMKEC PMKCET









@rmkceteev

@rmkceteev





To Apply Online



R.M.K. GROUP OF INSTITUTIONS

(Autonomous Institutions) (Approved by AICTE & Affiliated to Anna University)





B.E. ELECTRONICS ENGINEERING

(VLSI DESIGN AND TECHNOLOGY)

"Fostering VLSI engineers to shape the future of semiconductor technology."

R.M.K. ENGINEERING COLLEGE



R.M.K. COLLEGE OF ENGINEERING AND TECHNOLOGY



Knowledge Partner



Why B.E EE(VLSI Design and Technology) Unique at RMK Group?

- · Accredited by NBA & NAAC with A+ Grade and 151-200 Band in All India Ranking in NIRF 2024 (RMKEC) & 201-300 Band in NIRF Engineering Ranking 2024 (RMKCET)
- · Anna University Recognized Research Centre with 21 Supervisors for ECE Department.
- · EE (VLSI Design and Technology) undergraduate program presents a promising career path amidst increasing demand for VLSI professionals specializing in industrial chip design, priming them for impactful roles in semiconductor industries.
- Empower students in Cutting edge VLSI Laboratories with industry skills, fostering excellence and career readiness.
- Prepare students for VLSI careers, advanced academic pursuits and entrepreneurship, ensuring holistic approach for their success.
- First institution in India to receive Recognized Centre for TOEFL Certification Exam
- To pursue higher education in reputed Universities in India and abroad as VLSI is in great demand in companies like Intel, IBM, Cadence, Synopsis etc.,

Honours Degree Additional 18 Credits)

- Minor Degree (Additional 18 Credits)

Identification of VLSI Design and Technology

Identification of Industry Knowledge Partner

Identification of Faculty SPoC (Faculty should get certified in VLSI and Design Technology)

Nomination of an Expertise from Chosen Industry Knowledge Partner in Board of Studies

(minimum 40% of the curriculum and syllabi should be designed by knowledge Partner / Participation of Knowledge Partner Experts in Designing Honours & Minor Degree Courses)

Identification of Focus Group

Establishment of Industry Knowledge Partner Laboratories with required Tools

Participation of Nominated BoS Knowledge Partner

very of Syllabus/ ESE Question Paper Setting / ESE Central Valuation.

Technology Certifications / Summer & Winter Internships

ndustrial Visit / Guest lectures / Workshops / Masterclass / Bootcamp

Six months Internship & Hiring during VII Semester

Collaborative Research / Mentoring Startups / Setting up of industry Knowledge Partners Office Location at RMK

Salient Features

- Industry supported Autonomous curriculum (40% Syllabus provided by Tech Mahindra)
- An ISO 21001:2018 Certified Institution.
- VLSI Design Lab supported by Chips to Startup Programme (C2S) Ministry of Electronics and Information Technology, Government of India with Industry standard tools like Cadence, Synopsys, Siemens, Keysight, Ansys and Silvaco.
- VLSI Design and Technology specialization with 163 credits prepares students for careers in the semiconductor industry and they can work as VLSI design engineers, ASIC designers, physical design or verification engineers or in roles related to chip fabrication and testing.
- Curriculum delivery through Innovative Pedagogical techniques with Smart Classrooms, LCD projectors and Viewsonic
- · Live Interactive Assessment through RMK NextGen
- Activity Based Learning and Blended Learning / Project Based Learning
- Product Development Lab from Semester I to VI through AICTE Idea Lab
- Digital Course Materials designed and developed by our faculty team
- Transforms Idea to Prototype through AICTE sponsored IDEA LAB
- Counselling and Performance Monitoring through Pragati Online Counselling App
- Recognizing High Performers (RHP) & SkillRack Elite forum for Top Coders
- · Signed MoU with many reputed MNCs and foreign universities for collaboration
- · Achievements of students in Corporate Competitions and Hackathons
- 47 students were placed in the 2024 Batch with more than 5 LPA; Average Salary: 4 LPA
- · Our students have been recognized as Top 10 Learners in Infosys Springboard
- · First institution in India to receive Recognized Centre for TOEFL Certification Exam
- · Our students are the winners of Smart India Hackathon
- · Published more than 1000 papers in Scopus and Web of Science
- 25 Patents Granted and 128 Patents Published
- Organized International Admission Counselling Day
- · Organizing FDPs, Seminars, Workshops, Symposium and Conference every year
- Certification in Food Safety by FSSAI
- Engaging in outreach initiatives through the Unnat Bharat Abhiyan Village Adoption Scheme
- Co-curricular and extra-curricular talents showcased through 15 Student Clubs right from First Year.





Students with Dream offers

Ms. KESINENI DIVYA

₹ 66 Lakhs Per Annum



TCS DIGITAL ₹7 Lakhs Per Annum



Mr. KATTA MOHAN TCS PRIME ₹9 Lakhs Per

Annum



Mr. MUCKESH K S

PRESIDIO ₹ 9.5 Lakhs Per Annum

Industry Collaboration



Design

Domair

Signing

Association with Industry

o Elxsi, Wipro, Vantiva etc.,





Workshop

Epical

Layouts Private

o MoU signed with industries like Tech Mahindra, TATA

• Faculty training through Train-The-Trainer Model

Online certifications - Coursera, edX, NPTEL etc.,

Faculty certifications by the industry.

Hands on Workshop By Autobot Chenna



MoU with France

Universities

 Hackathons · Hands-on training by Industry



Industry Connect

Centre of Excellence Activities

Guest Lectures

Registration

· Faculty Development Programmes

· Mou Signed with Tech Mahindra

o Participating in KPIT- SPARKLE, Techgium Contest, SIH, etc.,





CENTRES OF EXCELLENCE

VLSI DESIGN (HCL)

Distinguished Alumni (ECE)

· Project expo for students.

Internship Opportunities



MR.ARUN PRAKASH T V PMTS, Oracle, Washington, USA



MS. ROSHINI

Assistant in Tamilnadu Secretariat



Mr.MICHAEL NADIN ANTONY Systems Enginee Alcatel - Lucent Washington, USA

Faculty Strength

(EE-VLSI-RMKEC & RMKCET)

Total Faculty

Doctarates

Pursuing Ph.D

Trained by Industry

10 Average Year Of Experience

Research Productivity 90%

Student Achievements (ECE)



Won 3rd prize, Rs.20,000 in *Umagine Chennai 2023 Naan Mudhalvan Tech show case Grand Finale* held at Chennai



SIH - 2023 - Senior Hardware Edition Winner - Prize Amount Of Rs. 1 Lakh, Held at Force Innovations - Coimbatore during 19 23 Dec, 2023



Third-year ECE students, won 2nd runner up in the Hackathon 2023 held at Oriental institute of science and technology, Bhopal



Mr. D. Prasad received ISTE Best student award 2022-23 on 21st Annual ISTE Tamil Nadu section Engineering Students convention

Our Major Recruiters

Placement and Training

2023 Batch

2021 Battl

2020 Batch

2019 Bato





*Placement in Progress (ECE)





90%









Consistent Placement

Business Unit Hiring

Companies Visited



1002 Hours of Phased Training Programs





Inauguration of VLSI Design Lab by

Mr. Vivekanand Challa, Head of **Engineering Services Talent Development**, **Tech Mahindra**

cprime >- hubstream Kaar LOS ZONIO Infosys

















